## In the Claims:

- (Previously Presented) An electronic device comprising a free-standing thin film, the thin film comprising an alloy of aluminum and at least magnesium.
- (Currently Amended) An electronic device as claimed in claim 1, eomprising wherein the alloy comprises an alloy of aluminum, magnesium and at least one further material.
- (Original) An electronic device as claimed in claim 2, wherein said at least one further material comprises one or more of copper, manganese, silicon, nickel, chromium, and lithium.
- (Previously Presented) An electronic device as claimed in claim 1, wherein the Mg content is between 0.1 and 10 atomic weight percent.
- (Original) An electronic device as claimed in claim 3, wherein said one further material comprises copper in an amount between 0.1 and 8 atomic weight percent.
- (Previously Presented) An electronic device as claimed in claim 3, wherein the sum of the contents of magnesium, copper and manganese is between 2.5 and 10 atomic weight percent.
- 7. (Currently Amended) An electronic device as claimed in claim 3, wherein the alloy is chosen from the group of  $Al_vMg_wCu_xMn_y$ ,  $Al_vMg_wMn_y$ ,  $Al_vMg_wCu_xSi_{z1}Ni_{z2}$ ,  $Al_vMg_wCu_x$ ,  $Al_vMg_wCu_xSi_{z1}$ ,  $Al_vMg_wCu_xSi_{z1}$ ,  $Al_vMg_wCu_xSi_{z1}$ ,  $Al_vMg_wCu_xSi_{z2}$ ,  $Al_vMg_wCu_xSi_{z2}$ ,  $Al_vMg_wCu_xSi_{z3}$ , with  $80 \le v \le 99.8$ ; and  $0.1 \le w \le 8.0$ ,  $0.1 \le x \le 8.0$ ,  $0.1 \le y \le 4.0$  and z1, z2, z3, z4, z5 each smaller than z0, and preferably smaller than z5.

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8. (Withdrawn – Currently Amended) A method of as claimed in claim 11, wherein manufacturing an the electronic device comprising a free-standing thin film the method comprising the steps of comprises:

providing a mechanical layer of material on a sacrificial release layer

structuring said mechanical layer to define the film and

removing said release layer to render said film free-standing, -characterized in that said

(Withdrawn) A method according to claim 8, wherein said release layer is patterned

top layer wherein the film comprises an the alloy of aluminum and at least magnesium.

prior to provision of said mechanical layer thereon.

- 10. (New) An electronic device as claimed in claim 7, wherein z1, z2, z3, z4, z5 are each smaller than 5
- (New) A method of manufacturing an electronic device comprising forming a freestanding thin film, the thin film comprising an alloy of aluminum and at least magnesium.
- (Withdrawn New) A method as claimed in claim 8, wherein the alloy comprises an alloy of aluminum, magnesium and at least one further material.
- (Withdrawn New) A method as claimed in claim 12, wherein the at least one further material comprises one or more of copper, manganese, silicon, nickel, chromium, and lithium.
- (Withdrawn New) A method as claimed in claim 8, wherein the Mg content is between
   and 10 atomic weight percent.

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- 15. (Withdrawn New) A method as claimed in claim 13, wherein the one further material comprises copper in an amount between 0.1 and 8 atomic weight percent.
- (Withdrawn New) A method as claimed in claim 13, wherein the sum of the contents of magnesium, copper and manganese is between 2.5 and 10 atomic weight percent.
- 17. (Withdrawn New) A method as claimed in claim 13, wherein the alloy comprises an alloy selected from the group consisting of  $Al_vMg_wCu_xMn_y$ ,  $Al_vMg_wMn_y$ ,  $Al_vMg_wCu_xSi_{z1}Ni_{z2}$ ,  $Al_vMg_wCu_x$ ,  $Al_vMg_wCu_xSi_{z1}$ ,  $Al_vMg_wCu_xZn_{z2}Cr_{z4}$ ,  $Al_vMg_wCu_xLi_{z5}$ , with  $80 \le v \le 99.8$ ; and  $0.1 \le w \le 8.0$ ,  $0.1 \le x \le 8.0$ ,  $0.1 \le y \le 4.0$  and z1, z2, z3, z4, z5 each smaller than 20.
- 18. (New) A MEMS device comprising:
  - a substrate;
  - a first electrode at an upper surface of the substrate; and
- a second electrode spaced from the first electrode by an air gap, the second electrode comprising a free-standing thin film comprising an alloy of aluminum and at least magnesium.
- 19. (New) A MEMS device as claimed in claim 18, wherein the substrate comprises a body and an insulating layer over the body, the first electrode formed over a surface of the insulating layer, wherein the first electrode is embedded in a passive network that comprises additional components.
- (New) A MEMS device as claimed in claim 18, further comprising a thin film capacitor at a surface of the substrate and laterally spaced from the first electrode.

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